FIG 1

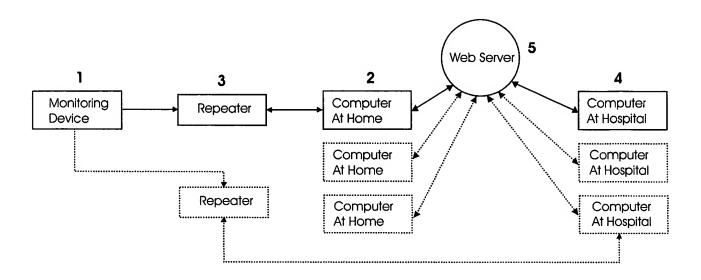
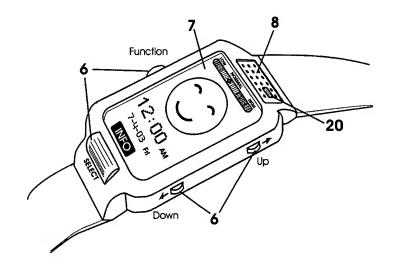
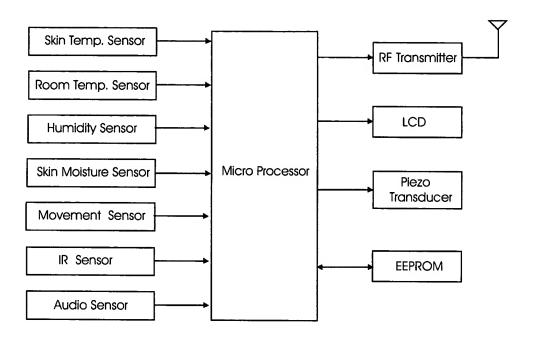
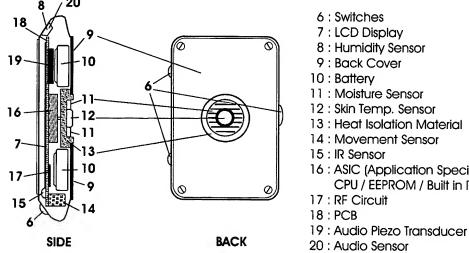


FIG 2





### **FIG 4**



16: ASIC (Application Specific Integrated Circuit) CPU / EEPROM / Built in Room Temp. Sensor

FIG 5

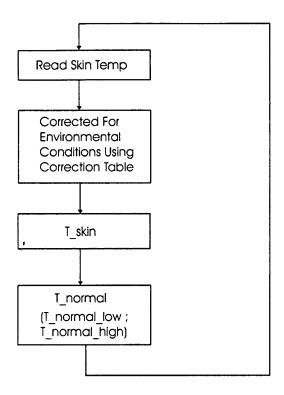


FIG 6

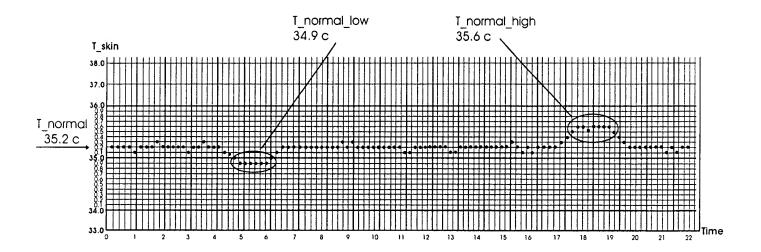


FIG 7

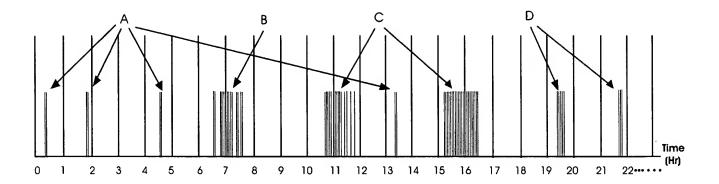
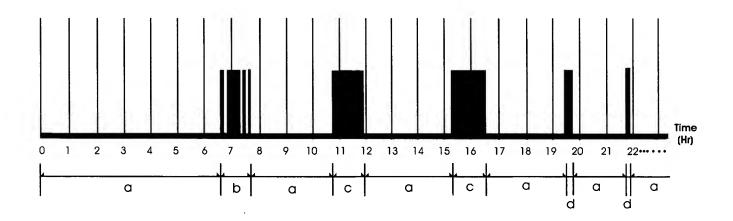
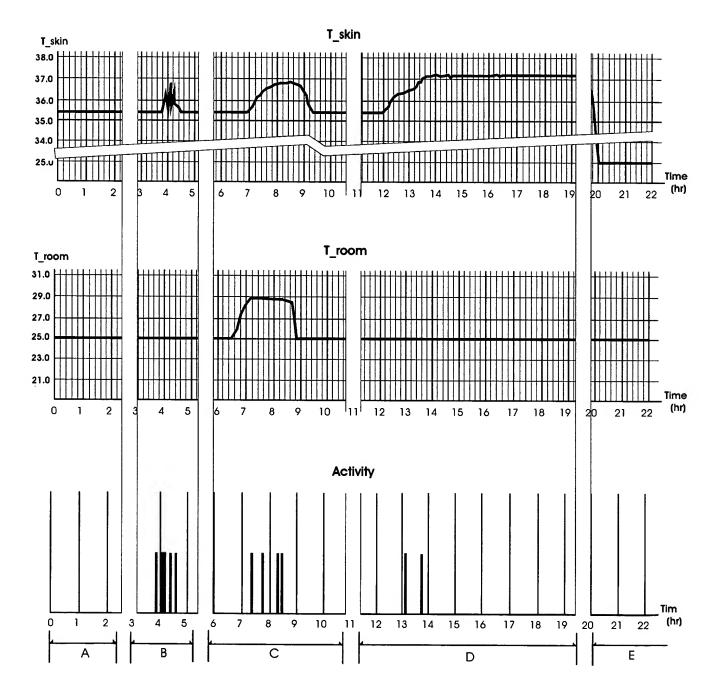
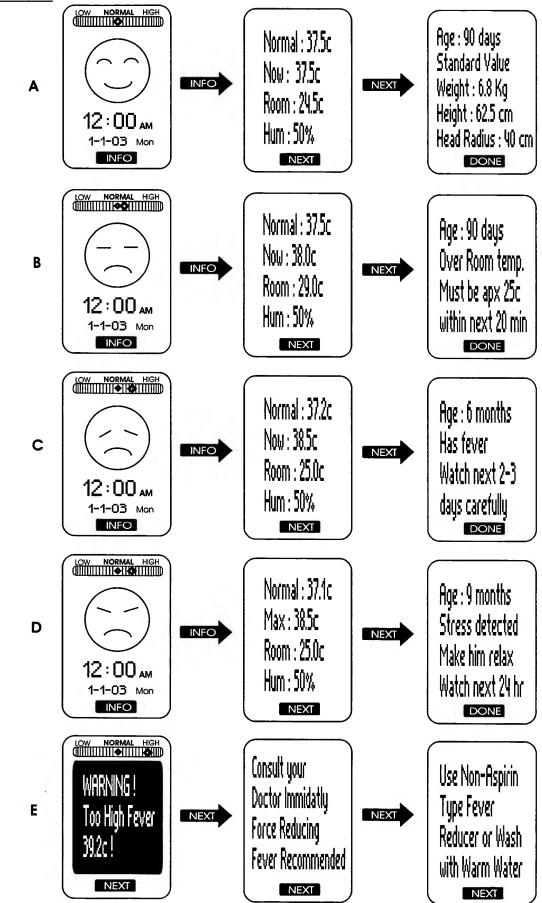
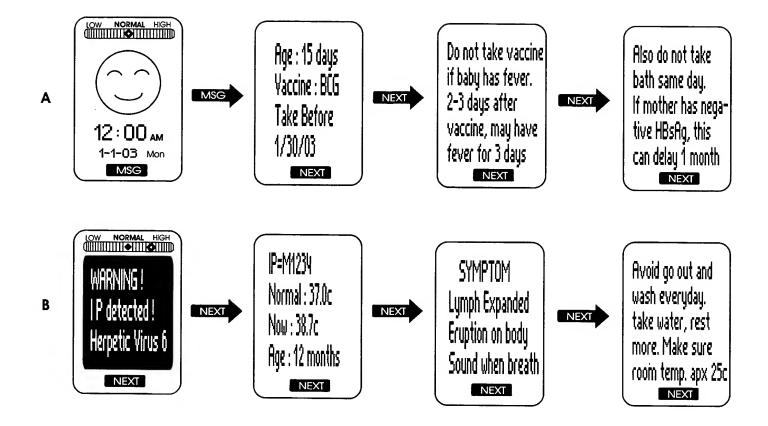


FIG 8









**FIG 12** 

